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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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09/823,802

03/30/2001

Gautam Dewan

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7405

32294 7590 01/24/2007
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EXAMINER

HO, DUC CHI

ART UNIT

PAPER NUMBER

2616

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
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3 MONTHS

01/24/2007

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary

Application No.

09/823,802

Applicant(s)

DEWAN ET AL.

Examiner

Duc C. Ho

Art Unit

2616

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 09 November 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 and 26-41 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 26-40 is/are allowed.
- 6) ☒ Claim(s) 1,2,7,8,13-19 and 41 is/are rejected.
- 7) ☒ Claim(s) 3-6, 9-12, and 20 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- ☐ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☐ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____
- ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- ☐ Notice of Informal Patent Application
- ☐ Other: _____

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains.

Patentability shall not be negated by the manner in which the invention was made.

2. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103© and potential 35 U.S.C. 102(f) or (g) prior art under 35 U.S.C. 103(a).

3. Claims 1-2, 7-8, 13-19, and 41 are rejected under 35 U.S.C. 103(a) as being unpatentable over Muller et al. (US 6,453,360), hereinafter referred to as Muller, in view of Steiss et al. (US 6,895,494), hereinafter referred to as Steiss.

Regarding claim 1, Muller discloses high performance network interface. In Muller the header parser 106-fig.3 is equivalent to the claimed limitation micro-controller. Instructions stored in instruction memory 306-fig.3 having a plurality of instruction fields-fig.23, wherein each instruction field related to a specific operation for parsing a packet, see col. 15, lines 33-62, col. 27, lines 28-32, and col. 30-line 20 to col. 34-line 55.

Muller, however, does not expressly disclose processing the plurality of instruction fields in parallel.

One skill in the art would recognize the advantage of processing a plurality of instruction fields in parallel since it offers an ability to exploit large amounts of instruction-level parallelism with relatively simple and inexpensive control hardware.

Steiss discloses sub-pipelined and pipelined execution in a VLIW. In Steiss Very Long Instruction Word (VLIW) processors are viewed as an attractive way of achieving instruction-level parallelism, see col. 1, lines 11-39, and col.1-line 11 to col.2-line 56.

At the time of the invention, it would have been obvious to a person of ordinary skill in the art to combine Muller with Steiss.

The suggestion/motivation for doing so would have been to offer an ability to exploit large amounts of instruction-level parallelism with relatively simple and inexpensive control hardware.

Therefore, it would have been obvious to combine Muller with Steiss to obtain the invention as specified in claim 1.

Regarding claim 2, in Muller the packet includes one or more protocol headers.

Regarding claims 7-8, these claims have similar limitations as claims 1-2. Therefore, they are rejected under Muller-Steiss for the same reasons as set forth in the rejection of claim 1-2.

Regarding claim 13, in Muller the header parser 106-fig.3 is a system on a chip.

Regarding claim 14, in Muller the memory 16-fig.3 stores data used in forming packets.

Regarding claim 15, Steiss discloses the VLIW architecture in which the instructions of Muller are based on.

Regarding claim 16, this claim has similar limitations as claim 1. Therefore, it is rejected under Muller-Steiss for the same reasons set forth in the rejection of claim 1.

Regarding claim 17, in Steiss the register set should include a checksum register.

Regarding claim 18, in Muller the operation should include a memory load and store operation.

Regarding claim 19, in Muller one of the execution units should be capable of performing the checksum operation.

Regarding claim 41, this claim has similar limitations as claim 1. Therefore, it is rejected under Muller-Steiss for the same reasons set forth in the rejection of claim 1.

Response to Arguments

4. Applicant's arguments filed 11-09-06 have been fully considered but they are not persuasive. Steiss discloses Very Long Instruction Word (VLIW) processors for processing instruction in parallel. Steiss reference is used to reject the VLIW engine in figure 1B of the instant application.

Allowable Subject Matter

5. Claims 26-39 are allowed.

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6. Claims 3-6, 9-12, and 20 are objected to as being independent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

7. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Duc Ho whose telephone number is (571) 272-3147. The examiner can normally be reached on Monday through Friday from 7:00 am to 3:30 pm.

If attempt to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wellington Chin, can be reached on (571) 272-3134.

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Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (571) 272-2600.

The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

9. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Patent Examiner



Duc Ho

1-19-07